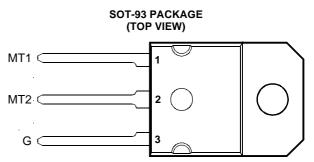
DECEMBER 1971 - REVISED MARCH 1997

- High Current Triacs
- 25 A RMS
- Glass Passivated Wafer
- 400 V to 800 V Off-State Voltage
- 175 A Peak Current
- Max I_{GT} of 50 mA (Quadrants 1 3)



Pin 2 is in electrical contact with the mounting base.

MDC2AD

absolute maximum ratings over operating case temperature (unless otherwise noted)

RATING			VALUE	UNIT	
	TIC263D		400		
Repetitive peak off-state voltage (see Note 1)	TIC263M	N/	600	V	
	TIC263S	V _{DRM}	700	v	
	TIC263N		800		
Full-cycle RMS on-state current at (or below) 70°C case temperature (see Note 2)			25	A	
Peak on-state surge current full-sine-wave (see Note 3)			175	А	
Peak gate current			±1	А	
Operating case temperature range			-40 to +110	°C	
Storage temperature range			-40 to +125	°C	
Lead temperature 1.6 mm from case for 10 seconds			230	°C	

NOTES: 1. These values apply bidirectionally for any value of resistance between the gate and Main Terminal 1.

- 2. This value applies for 50-Hz full-sine-wave operation with resistive load. Above 70°C derate linearly to 110°C case temperature at the rate of 625 mA/°C.
- 3. This value applies for one 50-Hz full-sine-wave when the device is operating at (or below) the rated value of peak reverse voltage and on-state current. Surge may be repeated after the device has returned to original thermal equilibrium.

electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS			MIN	ТҮР	MAX	UNIT
I _{DRM}	Repetitive peak off-state current	V _D = Rated V _{DRM}	$I_{G} = 0$	$T_{\rm C} = 110^{\circ}{\rm C}$			±2	mA
I _{GTM}		V _{supply} = +12 V†	$R_L = 10 \Omega$	t _{p(g)} > 20 μs		7	50	
	Peak gate trigger	V _{supply} = +12 V†	$R_L = 10 \ \Omega$	t _{p(g)} > 20 μs		-15	-50	
	current	$V_{supply} = -12 V^{\dagger}$	$R_L = 10 \ \Omega$	t _{p(g)} > 20 μs		-16	-50	ma
		V _{supply} = -12 V†	$R_L = 10 \ \Omega$	t _{p(g)} > 20 μs		28		
V _{GTM}		V _{supply} = +12 V†	R _L = 10 Ω	t _{p(g)} > 20 μs		0.7	2	V
	Peak gate trigger	V _{supply} = +12 V†	$R_L = 10 \ \Omega$	t _{p(g)} > 20 μs		-0.7	-2	
	voltage	$V_{supply} = -12 V^{\dagger}$	$R_L = 10 \ \Omega$	t _{p(g)} > 20 μs		-0.8	-2	
		V _{supply} = -12 V†	$R_L = 10 \ \Omega$	t _{p(g)} > 20 μs		0.8	2	
V _{TM}	Peak on-state voltage	$I_{TM} = \pm 35.2 \text{ A}$	l _G = 50 mA	(see Note 4)		±1.5	±1.7	V
I _H	Holding current	V _{supply} = +12 V†	$I_{G} = 0$	Init' I _{TM} = 100 mA		6	40	mA
	ribiding cullent	V _{supply} = -12 V†	$I_G = 0$	Init' I _{TM} = -100 mA		-13	-40	ШA

† All voltages are with respect to Main Terminal 1.

NOTE 4: This parameter must be measured using pulse techniques, $t_p = \le 1$ ms, duty cycle ≤ 2 %. Voltage-sensing contacts separate from the current carrying contacts are located within 3.2 mm from the device body.

PRODUCT INFORMATION

Information is current as of publication date. Products conform to specifications in accordance with the terms of Power Innovations standard warranty. Production processing does not necessarily include testing of all parameters.



DECEMBER 1971 - REVISED MARCH 1997

electrical characteristics at 25°C case temperature (unless otherwise noted) (continued)

	PARAMETER	TEST CONDITIONS		MIN	ТҮР	МАХ	UNIT	
ΙL	Latching current	$V_{supply} = +12 V^{+}$ $V_{supply} = -12 V^{+}$	(see Note 5)			20 -20		mA
dv/dt	Critical rate of rise of off-state voltage	V _D = Rated V _D	I _G = 0	T _C = 110°C		±450		V/µs
dv/dt _(c)	Critical rise of commutation voltage	V_D = Rated V_D di/dt = 0.5 I _{T(RMS)} /ms		T _C = 80°C I _T = 1.4 I _{T(RMS)}		±1		V/µs
di/dt	Critical rate of rise of on -state current	V _D = Rated V _D di _G /dt = 50 mA/µs	I _{GT} = 50 mA	T _C = 110°C		±200		A/µs

† All voltages are with respect to Main Terminal 1.

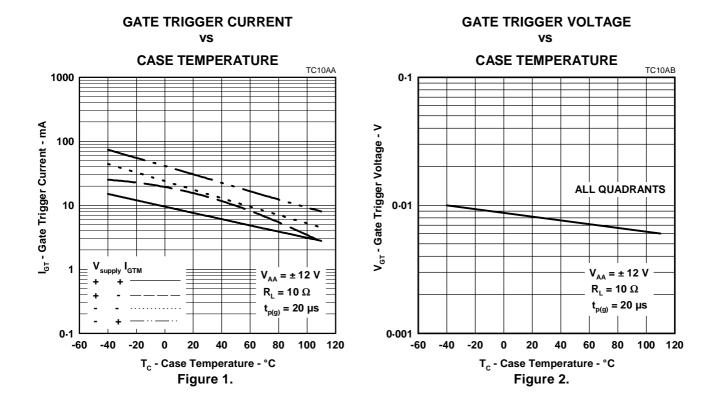
5: The triacs are triggered by a 15-V (open-circuit amplitude) pulse supplied by a generator with the following characteristics: $R_G = 100 \Omega$, $t_{p(g)} = 20 \mu s$, $t_r = \le 15 ns$, f = 1 kHz.

thermal characteristics

NOTE

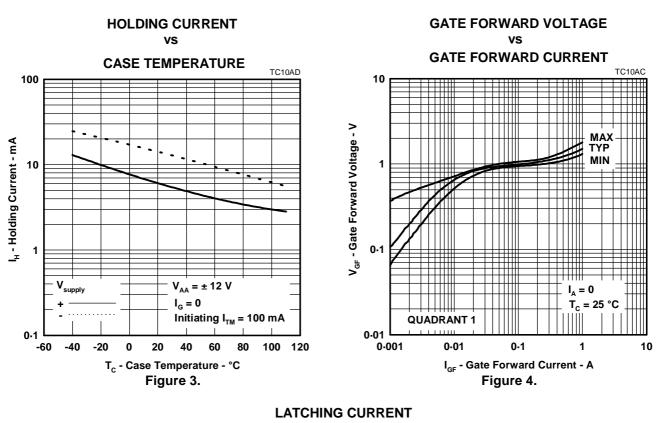
PARAMETER			TYP	MAX	UNIT
R_{\thetaJC}	Junction to case thermal resistance			1.52	°C/W
R_{\thetaJA}	Junction to free air thermal resistance			36	°C/W

TYPICAL CHARACTERISTICS



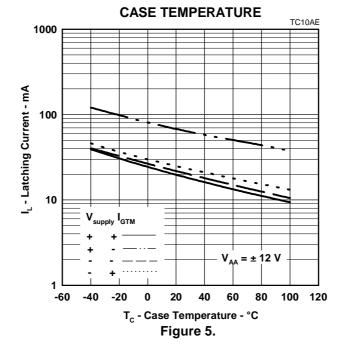
PRODUCT INFORMATION

DECEMBER 1971 - REVISED MARCH 1997



TYPICAL CHARACTERISTICS

vs





PRODUCT INFORMATION

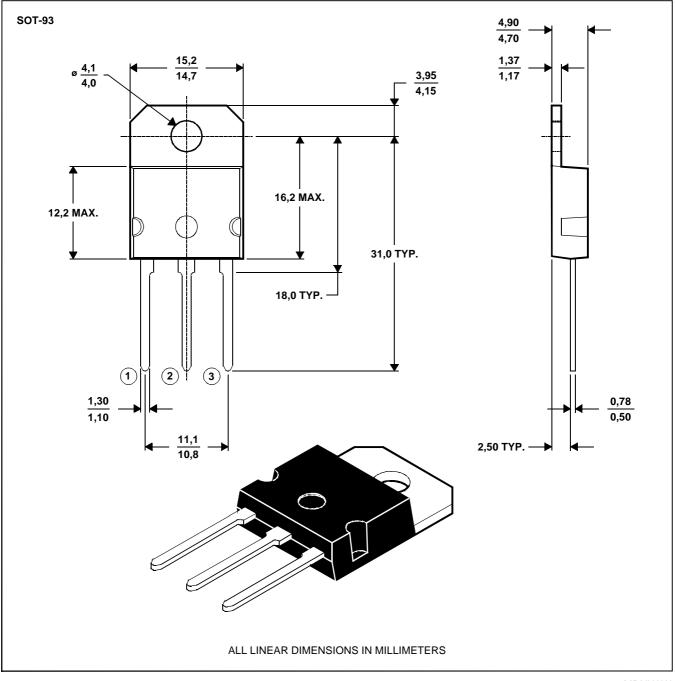
DECEMBER 1971 - REVISED MARCH 1997

MECHANICAL DATA

SOT-93

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

MDXXAW

PRODUCT INFORMATION

DECEMBER 1971 - REVISED MARCH 1997

IMPORTANT NOTICE

Power Innovations Limited (PI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to verify, before placing orders, that the information being relied on is current.

PI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with PI's standard warranty. Testing and other quality control techniques are utilized to the extent PI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except as mandated by government requirements.

PI accepts no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor is any license, either express or implied, granted under any patent right, copyright, design right, or other intellectual property right of PI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

PI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS.

Copyright © 1997, Power Innovations Limited



PRODUCT INFORMATION

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.