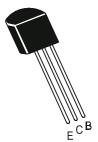




An IS/ISO 9002 and IECQ Certified Manufacturer

### NPN SILICON PLANAR EPITAXIAL TRANSISTORS



BC167A, BC167B BC168A, BC168B, BC168C BC169B, BC169C

TO-92 Plastic Package

# AF Pre and Driver Stages as well as for Universal Application.

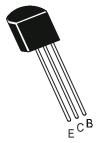
ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	BC167	BC168	BC169	UNITS
Collector -Emitter Voltage	$V_{CEO}$	45	20	20	V
Collector -Emitter Voltage	$V_{CES}$	50	30	30	V
Emitter -Base Voltage	$V_{EBO}$	6.0	5	5	V
<b>Collector Current Continuous</b>	I <sub>C</sub>	100	100	50	mA
<b>Collector Peak Current</b>	$I_{CM}$	200	200		mA
Base Current	$I_{B}$	50	50	5	mA
Power Dissipation @ Ta=25°C	P <sub>tot</sub>		300		mW
Storage Junction	$T_{stg}$		-55 to +15	0	٥С
Junction Temperature	$T_{j}$		150		°С
THERMAL RESISTANCE					
Junction to Ambient	$R_{th(j-a)}$		420		K/W

**ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)** 

DESCRIPTION	,	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
<b>Collector -Emitter Voltage</b>	BC167	BV <sub>CEO</sub>	$I_C=2mA,I_B=0$	45			V
	BC168, 169			20			V
Emitter-Base Voltage	BC167	$BV_{EBO}$	$I_E = 1 \mu A, I_C = 0$	6			V
	BC168, 169			5			V
Collector-Cut off Current							
	BC167	$I_{CES}$	$V_{CE}$ =50 $V$ , $V_{BE}$ =0			15	nA
	BC168, 169		$V_{CE}$ =30 $V$ , $V_{BE}$ =0			15	nA
			Ta =125°C				
	BC167		$V_{CE}$ =50 $V$ , $V_{BE}$ =0			4	μΑ
	BC168, 169		$V_{CE}=30V, V_{BE}=0$			4	μΑ

## NPN SILICON PLANAR EPITAXIAL TRANSISTORS



BC167A, BC167B BC168A, BC168B, BC168C BC169B, BC169C

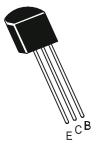
TO-92 Plastic Package

**ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)** 

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DC Current Gain A	h <sub>FE</sub>	I <sub>C</sub> =0.01mA,V <sub>CF</sub> =5V		90		
B		.C 0.0 4, * CE 0 .		150		
C				270		
C				210		
Α		$I_C=2mA, V_{CE}=5V$	120		220	
В			180		460	
С			380		800	
BC167A, 168A		$I_C=100$ mA, $V_{CE}=5$ V		120		
BC167B, 168B				200		
BC168C				400		
Collector Emitter Saturation Voltage	V <sub>CE(Sat)</sub> *	$I_C=10$ mA, $I_B=0.5$ mA			0.2	V
		$I_{C}=100mA, I_{B}=5mA^{**}$			0.6	V
	0_(00.)					
Base Emitter Saturation Voltage	V <sub>BE(Sat)</sub> *	$I_C=10$ mA, $I_B=0.5$ mA			0.83	V
	V <sub>RF(Sat)</sub> *	$I_C=100$ mA, $I_B=5$ mA**			1.05	V
	DE(Gat)	, ,				
Base Emitter On Voltage	$V_{BE(On)}$	$I_C=2mA, V_{CE}=5V$	0.55		0.7	V
_	()	$I_C=0.1$ mA, $V_{CF}=5$ V		0.55		V
		$I_{C}=100\text{mA}, V_{CF}=5V^{**}$		0.83		V

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DYNAMIC CHARACTERISTICS						
Transistors Frequency	f <sub>T</sub>	$I_C$ =0.5mA, $V_{CE}$ =3V f=100MHz		85		MHz
		$I_C=10$ mA, $V_{CE}=5$ V f=100MHz	150			MHz
Collector Capacitance	$C_{\sf cbo}$	$V_{CB}$ =10V, $I_{E}$ =0 f=1MHz			4.5	pF
Emitter Capaitance	$C_{ebo}$	$V_{EB} = 0.5V$ , $f = 1MHz$		8.0		pF

### NPN SILICON PLANAR EPITAXIAL TRANSISTORS



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TO-92 Plastic Package

DESCRIPTION		SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DYNAMIC CHARACTERISTICS							
Noise Figure							
E	3C169	NF	$I_C=0.2$ mA, $V_{CE}=5$ V			4.0	dB
Small Signall Current Gain	Α		Rg=2W, f=30HZ to 15kHz				
BC16	7, 168	NF	$I_C=0.2$ mA, $V_{CE}=5$ V			10	
BC16	69		Rg=2W, f=1kHZ , f=200Hz			4	dB dB
Small Signall Current Gain	Α	h <sub>11e</sub>	$I_C=2mA, V_{CE}=5V,$	1.6		4.5	kW
_	В		f=1kHz	3.2		8.5	kW
	С			6.0		16	kW
	A B C				1.5 2.0 3.0		10 <sup>-4</sup>
	Α	h <sub>21e</sub>		125		260	
	В			240		500	
	С			450		900	
	Α	h <sub>22e</sub>				30	$\mu$ MHO
	В					60	$\mu$ MHO
	С					110	μ ΜΗΟ

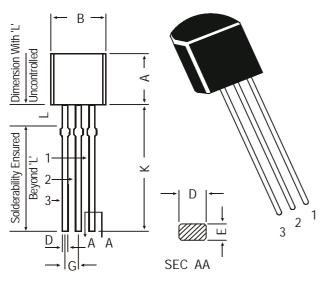
<sup>\*\*</sup> Measuring Values not for BC169

<sup>\*</sup> The transistor is overdriven to such an extent that the static forward current transfer ratio has decreased to  $h_{\text{FE}}$  =20

## **TO-92 Plastic Package**

## **TO-92 Plastic Package**

#### **TO-92 Transistors on Tape and Ammo Pack**



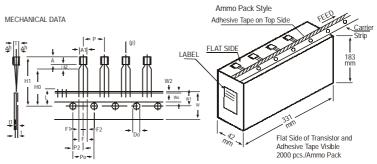
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# PIN CONFIGURATION

- 1. BASE
- 2. COLLECTOR
- 3. EMITTER

DIM	MIN.	MAX.
Α	4.32	5.33
В	4.45	5.20
С	3.18	4.19
D	0.41	0.55
Ε	0.35	0.50
F	5 D	EG
G	1.14	1.40
Н	1.14	1.53
K	12.70	_
L	1.982	2.082
		•

All diminsions in mm.



#### All dimensions in mm unless specified otherwise

ITFM			SPECIF	ICATIO	N	251112112
I I E IVI	SYMBOL	MIN.	NOM. MAX. 1		TOL .	REMARKS
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT	Α	4.8		5.2		
BODY THICKNESS	Ţ	3.9		4.2		
PITCH OF COMPONENT	P		12.7		±1	CUMULIATIVE DITCU
FEED HOLE PITCH	Po		12.7		±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
FEED HOLE CENTRE TO						
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER					+0.6	
LEADS	F		5.08	1	-0.2	4T TOD OF DODY
COMPONENT ALIGNMENT	△h		0 18	1	٥٦	AT TOP OF BODY
TAPE WIDTH HOLD-DOWN TAPF WIDTH	W Wo		6		±0.5 ±0.2	
HOLE-DOWN TAFE WIDTH	W1		9		+0.7	
HOLET OSHION	VVI		,		-0.5	
HOLD-DOWN TAPE POSITION	W2		0.5		±0.2	
LEAD WIRE CLINCH HEIGHT	Ho		16		±0.5	
COMPONENT HEIGHT	H1			23.25		
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	Do		4	1.2	±0.2	+1 0 2 0 /
TOTAL TAPE THICKNESS LEAD - TO - LEAD DISTANCEF1,	t F2		2.54	1.2	+0.4	t1 0.3 - 0.6
LEAD - TO - LEAD DISTANCEFT,	гΖ		2.34		+0.4 -0.1	
CLINCH HEIGHT	H2			3	0.1	
PULL - OUT FORCE	(P)	6N				

- MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
  MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20
  PITCHES.
- PITCHES.
  HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
  NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.
  A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.
  SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

# Packing Detail

Tuoking Dotain										
PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX					
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt			
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs			
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs			

**Notes** 

BC167A, BC167B BC168A, BC168B, BC168C BC169B, BC169C

TO-92 Plastic Package

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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