

WESTCODE SEMICONDUCTORS

Technical
Publication
TN281C

Issue 1
April 1985

Converter Grade Capsule Thyristor Type N281C 680 amperes average: up to 1800 volts V_{RRM}

Ratings (Maximum values at 125°C Tj unless stated otherwise)

RATING	CONDITIONS	SYMBOL	
Average on-state current	Half sine wave { 55°C heatsink temperature (double side cooled) 85°C heatsink temperature (single side cooled)	$I_{T(AV)}$	680 A
R.M.S. on-state current	25°C heatsink temperature, double side cooled	$I_T(RMS)$	1350 A
Continuous on-state current	25°C heatsink temperature, double side cooled	I_T	1140 A
Peak one-cycle surge (non-repetitive) on state current	10ms duration, 60% V_{RRM} re-applied	$I_{TSM(1)}$	7500 A
Maximum permissible surge energy	10ms duration, $V_R \leq 10$ volts	$I_{TSM(2)}$	8300 A
	10ms duration, $V_R \leq 10$ volts	$I_{R(2)}$	345000 A ²
Peak forward gate current	Anode positive with respect to cathode	I_{FGM}	20 A
		V_{FGM}	18 V
Peak forward gate voltage	Anode positive with respect to cathode	V_{RGM}	5 V
Peak reverse gate voltage		P_G	2 W
Average gate power		P_{GM}	100 W
Peak gate power	100µs. pulse width	dv/dt	*200 V/µs
Rate of rise of off-state voltage	To 80% V_{DRM} gate open-circuit	$di/dt(1)$	500 A/µs
Rate of rise of on-state current (repetitive)	} Gate drive 20 volts, 20 ohms with $t_r \leq 1\mu s.$ } Anode voltage $\leq 80\% V_{DRM}$	$di/dt(2)$	1000 A/µs
Rate of rise of on-state current (non-repetitive)			
Operating temperature range		T_{hs}	-40 + 125°C
Storage temperature range		T_{stg}	-40 + 150°C

Characteristics (Maximum values at 125°C Tj unless stated otherwise)

CHARACTERISTIC	CONDITIONS	SYMBOL	
Peak on-state voltage	At 1550 A, I_{TM}	V_{TM}	2.0 V
Forward conduction threshold voltage		V_O	1.09 V
Forward conduction slope resistance		r	0.587 mΩ
Repetitive peak off-state current	At V_{DRM}	I_{DRM}	40 mA
Repetitive peak reverse current	At V_{RRM}	I_{RRM}	40 mA
Maximum gate current required to fire all devices	} $V_A = 6 V, I_A = 1 A$ at 25°C Tj	I_{GT}	150 mA
Maximum gate voltage required to fire all devices		V_{GT}	3 V
Maximum holding current		I_H	500 mA
Maximum gate voltage which will not trigger any device		V_{GD}	0.25 V
Thermal resistance, junction to heatsink, for a device with a maximum forward volt drop characteristic	Double side cooled	$R_{th(j-hs)}$	0.05°C/W
	Single side cooled		0.1°C/W

VOLTAGE CODE		H02	H04	H06	H08	H10	H12	H14	H16	H18
Repetitive peak voltages	V_{RRM} V_{DRM}									
Non-repetitive peak off-state voltage	V_{DSM}	200	400	600	800	1000	1200	1400	1600	1800
Non-repetitive peak reverse blocking voltage	V_{RSM}	300	500	700	900	1100	1300	1500	1700	1900

Ordering Information (Please quote device code as explained below - 8 digits)

N 2 8 1 C	● ● ●	
2220 E-03	Voltage code (see ratings)	Typical code: N281CH16 = 1600 V_{RRM} 1600 V_{DRM} , 200 V/µs dv/dt to 80% V_{DRM}

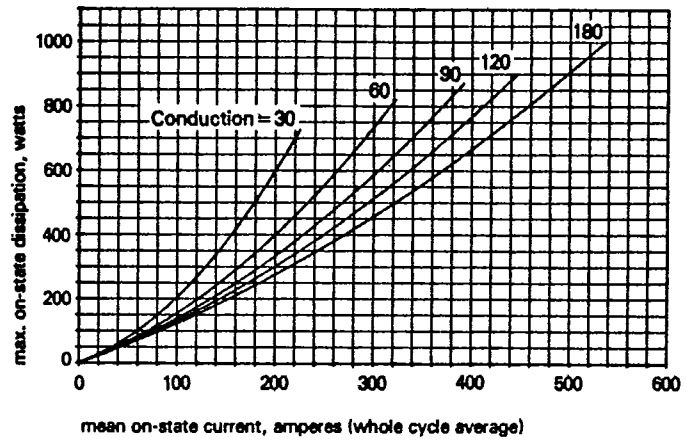
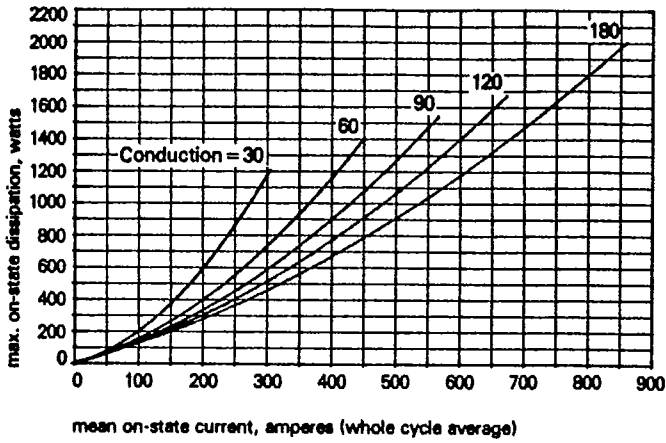
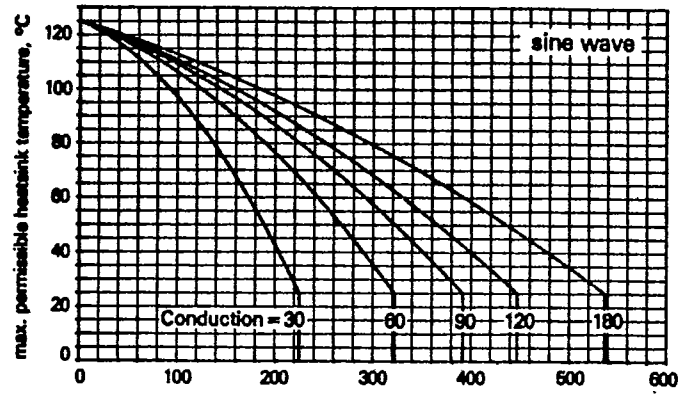
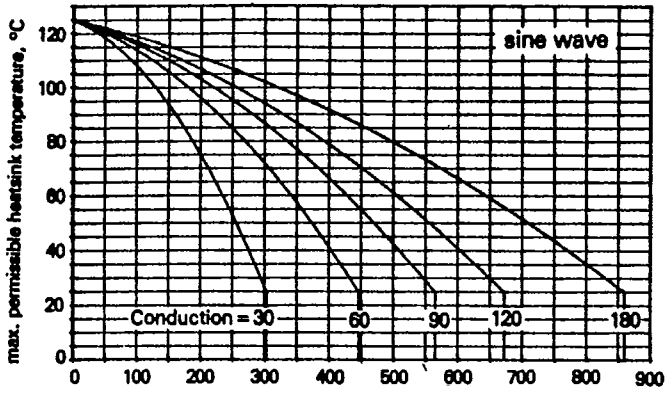


Figure 1 Dissipation and heatsink temperature v. current (Double side cooled)

Figure 2 Dissipation and heatsink temperature v. current (Single side cooled)

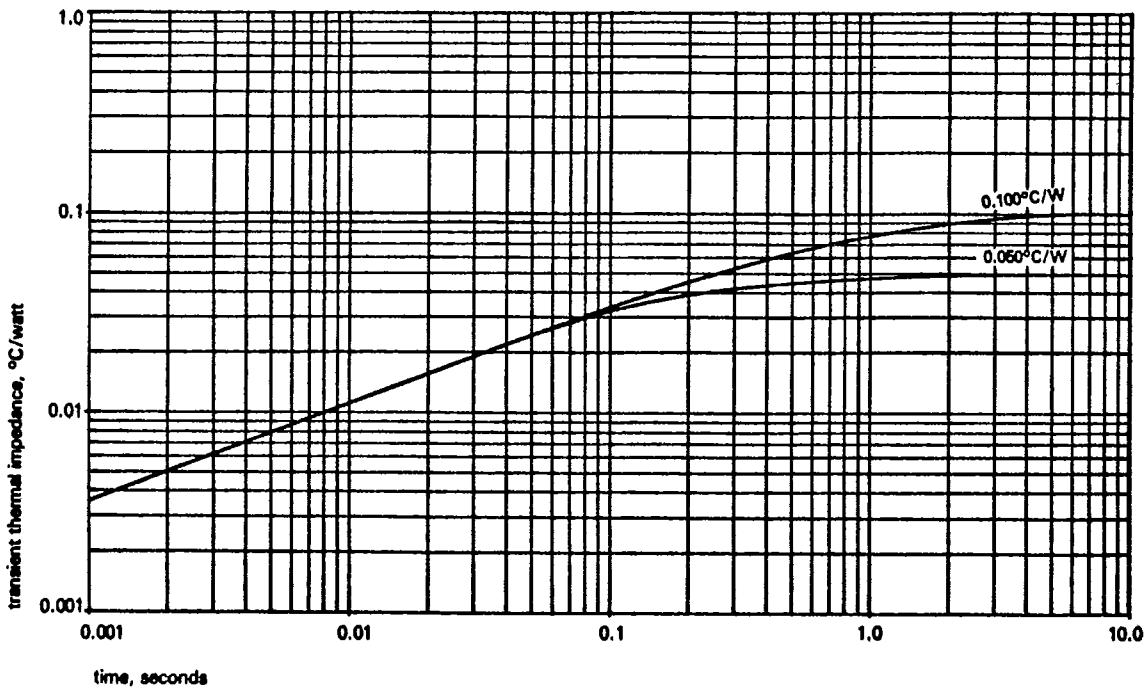


Figure 3 Junction to heatsink thermal impedance

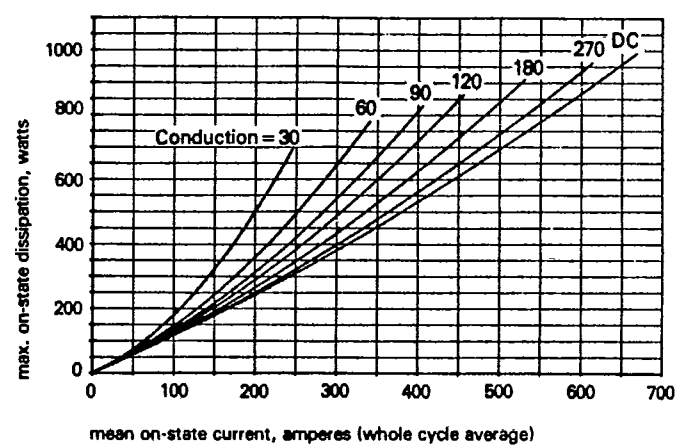
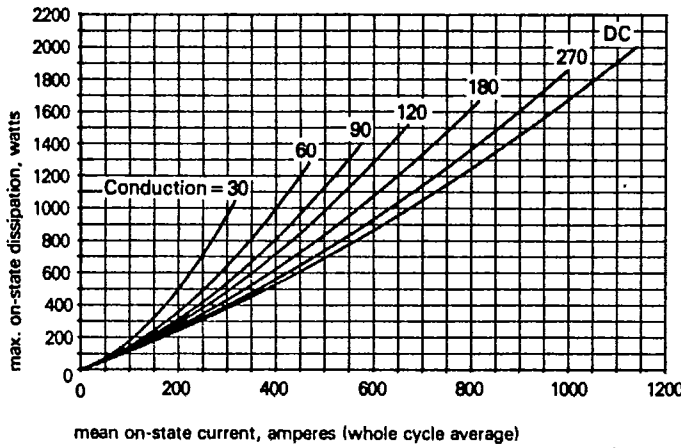
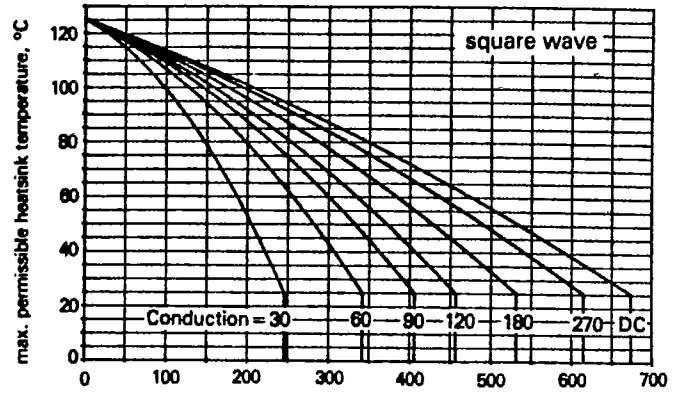
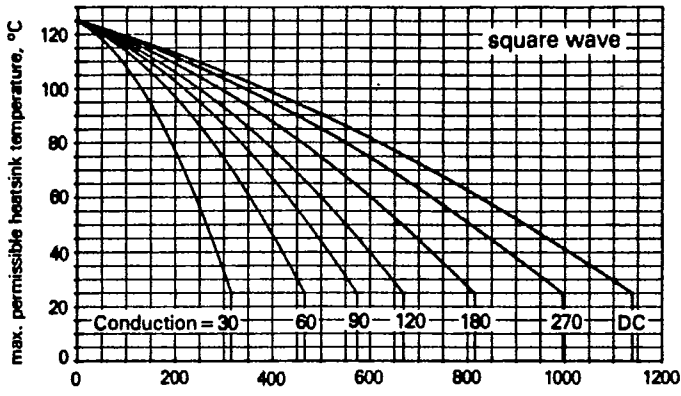


Figure 4 Dissipation and heatsink temperature v. current (Double side cooled)

Figure 5 Dissipation and heatsink temperature v. current (Single side cooled)

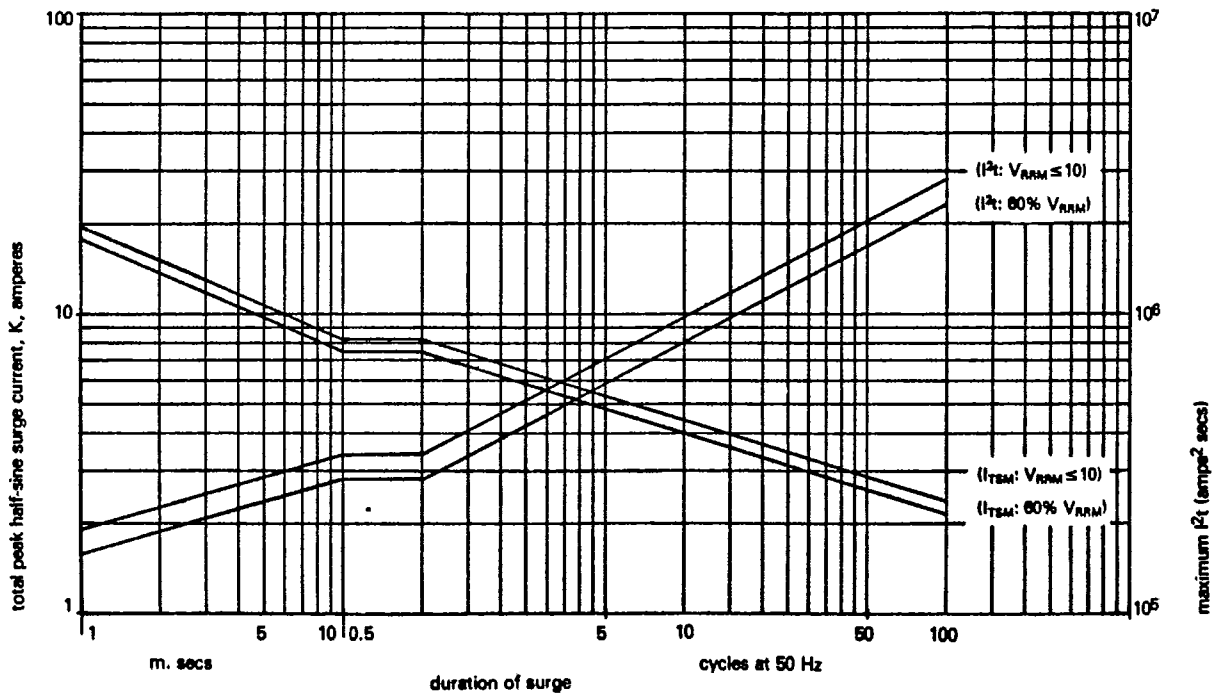


Figure 6 Max. non-repetitive surge current at initial junction temperature 125°C.
 (gate may temporarily lose control of firing angle)
 Note: This rating must not be interpreted as an intermittent rating

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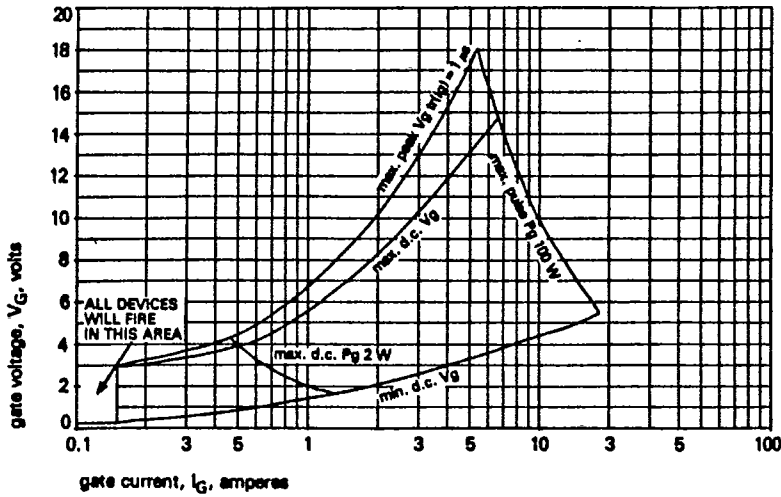


Figure 7 Gate characteristics at 25°C junction temperature

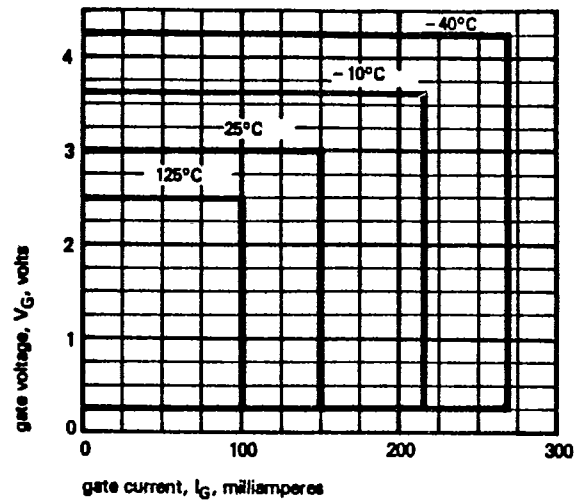


Figure 8 Gate triggering characteristics
Trigger points of all thyristors lie within the areas shown

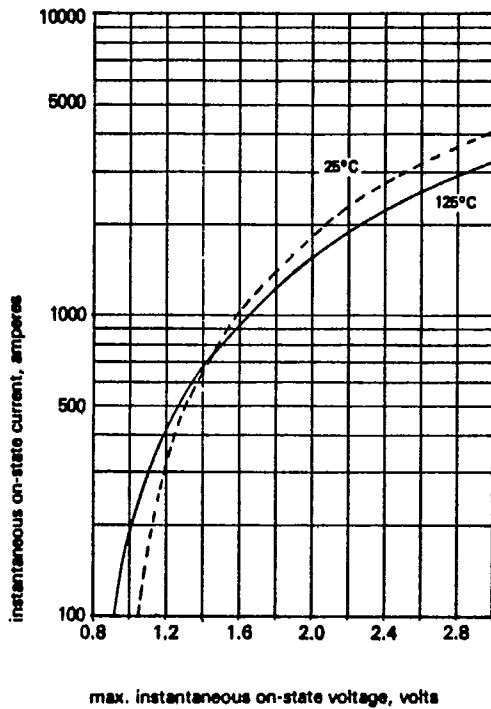
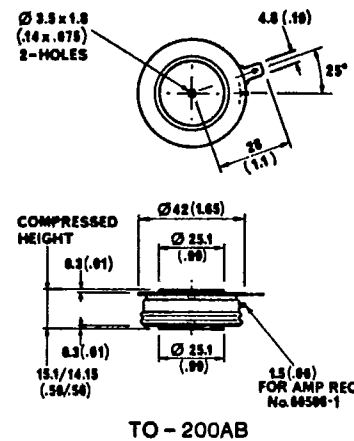


Figure 9 Limit on-state characteristic



Dimensions in mm (inches)
Mounting force: 530-1000 Kgf
Weight: 90 grams

In the interest of product improvement, Westcode reserves the right to change specifications at any time without notice.

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