18 W BTL Audio Power Amplifier

HITACHI

ADE-207-329 (Z)

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Description

The HA13118 is power IC designed for component car stereo amplifiers. At 13.2 V to 4 Ω load, this power IC provides an output power of 18W with 10% distortion.

It is easy to design as this IC employs internal each protection circuit and the new small package.

Features

- Small outline package, easy to mount
- Internal each protection circuits
 - Surge protection circuit
 - Thermal shut-down circuit
 - Ground fault protection circuit
 - Power supply fault protection circuit

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

| Symbol | Rating | Unit | Note |
|------------------------|--|---|---|
| V _{cc} | 18 | V | |
| V _{cc} (DC) | 26 | V | 1 |
| V _{cc} (peak) | 50 | V | 2 |
| lo (peak) | 4 | А | |
| P _T | 15 | W | |
| θj — c | 3.5 | °C/W | |
| Тј | 150 | °C | |
| Topr | -30 to +80 | °C | |
| Tstg | -55 to +125 | °C | |
| | V_{cc} $V_{cc} (DC)$ $V_{cc} (peak)$ $lo (peak)$ P_{T} $\theta j - c$ $T j$ $Topr$ | V _{cc} 18 V_{cc} (DC) 26 V_{cc} (peak) 50 lo (peak) 4 P_{T} 15 $\theta j - c$ 3.5 Tj 150 Topr -30 to +80 | V _{cc} 18 V V_{cc} (DC) 26 V V_{cc} (peak) 50 V lo (peak) 4 A P_{τ} 15 W $\theta j - c$ 3.5 °C/W Tj 150 °C Topr -30 to +80 °C |

Notes: 1. Value at t = 30 sec.

2. Value at width tw = 200 ms and rise time tr = 1 ms.



Electrical Characteristics (V $_{\rm CC}$ = 13.2 V, f = 1 kHz, R $_{\rm L}$ = 4 $\Omega,$ Ta = 25 °C)

| Item | Symbol | Min | Тур | Max | Unit | Test Conditions |
|--------------------------------|----------------|-----|-----|------|------|--|
| Quiescent current | Ι _α | 40 | 80 | 160 | mA | Vin = 0 |
| Input bias voltage | V _B | | 20 | 40 | mV | Vin = 0 |
| Output offset voltage | ΔV_{Q} | _ | | +330 | mV | Vin = 0 |
| Voltage gain | G _v | 53 | 55 | 57 | dB | Vin = -55 dBm |
| Output power | Pout | 15 | 18 | _ | W | $THD = 10~\% \qquad R_{\scriptscriptstyle L} = 4~\Omega$ |
| | | _ | 11 | _ | | $R_{L} = 8 \Omega$ |
| Total harmonic distortion | THD | | 0.2 | 1.0 | % | Pout = 1.5 W |
| Output noise voltage | WBN | _ | 1.0 | 2.0 | mV | Rg = 10 kΩ, BW = 20 Hz 20 kHz |
| Supply voltage rejection ratio | SVR | 33 | 44 | — | dB | f = 500 Hz |
| Input resistance | Rin | 20 | 30 | 40 | kΩ | |
| Rolloff frequency | f | _ | 20 | _ | Hz | $\Delta Gv = -3 dB$ Low |
| | f _H | 10 | 20 | 40 | kHz | from f = 1 kHz Ref. High |

Block Diagram

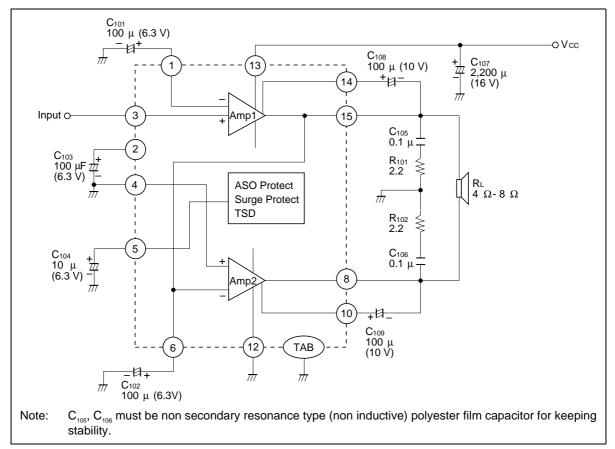
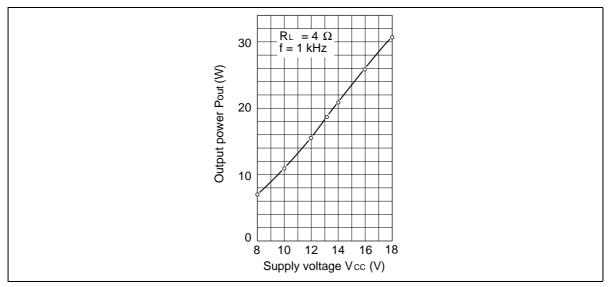


Figure 1 Typical Application Circuit





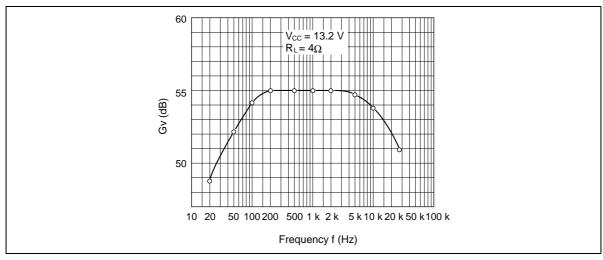
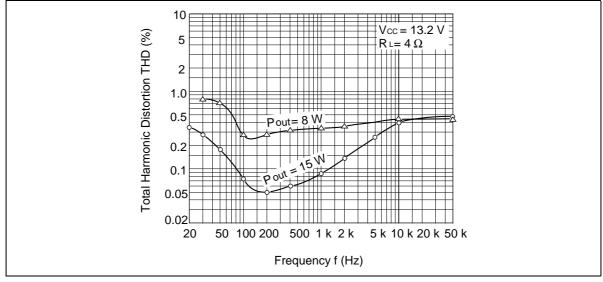
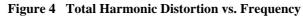


Figure 3 Voltage Gain vs. Frequency





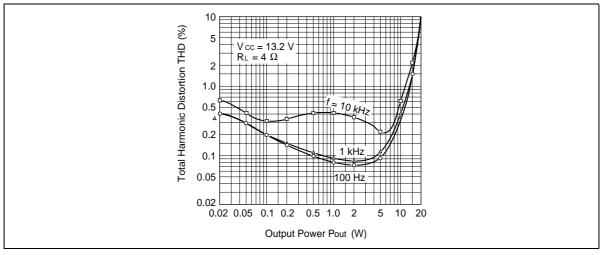


Figure 5 Total Harmonic Distortion vs. Output Power

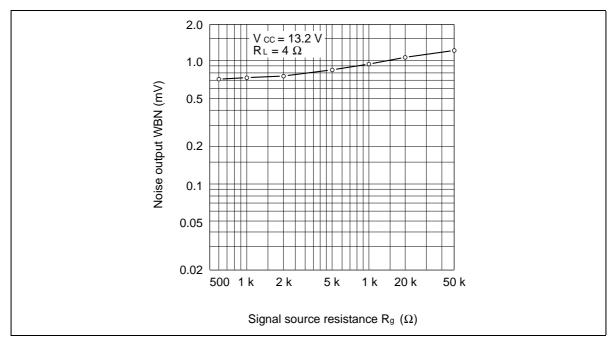


Figure 6 Noise Output vs. Signal Source Resistance

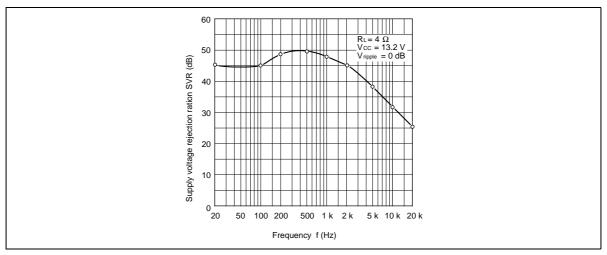
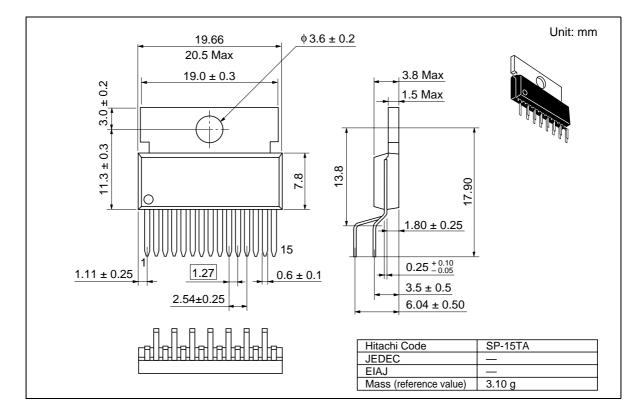


Figure 7 Supply Voltage Rejection Ratio vs. Frequency



Package Dimensions

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