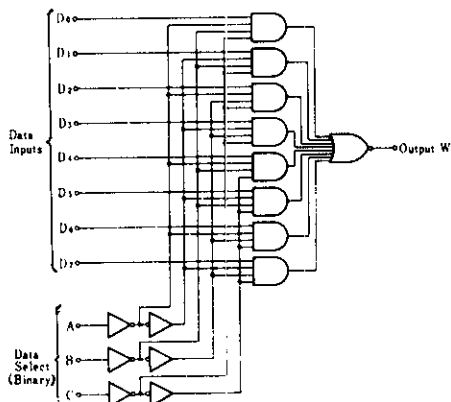


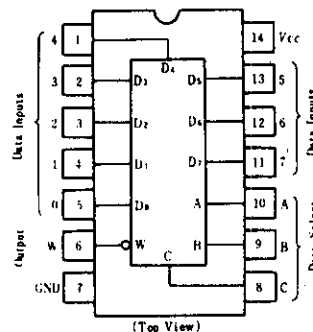
# HD74LS152 • 1-of-8 Data Selectors/Multiplexers

This data selector/multiplexer contains full-on-chip binary decoding to select the desired data source. The HD74LS152 selects one-of-eight data sources.

## ■ BLOCK DIAGRAM



## ■ PIN ARRANGEMENT



## ■ FUNCTION TABLE

Select inputs			Output	Select inputs			Output
C	B	A	W	C	B	A	W
L	L	L	$\overline{D}_0$	H	L	L	$\overline{D}_4$
L	L	H	$\overline{D}_1$	H	L	H	$\overline{D}_5$
L	H	L	$\overline{D}_2$	H	H	L	$\overline{D}_6$
L	H	H	$\overline{D}_3$	H	H	H	$\overline{D}_7$

Notes)  $\overline{D}_0 \sim \overline{D}_7$ ; the level of the D respective input  
 H; high level  
 L; low level

## ■ ELECTRICAL CHARACTERISTICS ( $T_a = -20 \sim +75^\circ\text{C}$ )

Item	Symbol	Test Conditions	min	typ*	max	Unit	
Input voltage	$V_{IH}$		2.0	—	—	V	
	$V_{IL}$		—	—	0.8	V	
Output voltage	$V_{OH}$	$V_{CC} = 4.75\text{V}, I_{OH} = -400\mu\text{A}, V_{IH} = 2\text{V}, V_{IL} = 0.8\text{V}$	2.7	—	—	V	
	$V_{OL}$	$V_{CC} = 4.75\text{V}, V_{IH} = 2\text{V}, V_{IL} = 0.8\text{V}$	$I_{OL} = 4\text{mA}$	—	—	0.4	V
			$I_{OL} = 8\text{mA}$	—	—	0.5	
Input current	$I_{IH}$	$V_{CC} = 5.25\text{V}, V_I = 2.7\text{V}$	—	—	20	$\mu\text{A}$	
	$I_{IL}$	$V_{CC} = 5.25\text{V}, V_I = 0.4\text{V}$	—	—	-0.4	mA	
	$I_I$	$V_{CC} = 5.25\text{V}, V_I = 7\text{V}$	—	—	0.1	mA	
Short-circuit output current	$I_{OS}$	$V_{CC} = 5.25\text{V}$	-20	—	-100	mA	
Supply current**	$I_{CC}$	$V_{CC} = 5.25\text{V}$	—	6.0	10	mA	
Input clamp voltage	$V_{IK}$	$V_{CC} = 4.75\text{V}, I_{IN} = -18\text{mA}$	—	—	-1.5	V	

\*  $V_{CC} = 5\text{V}, T_a = 25^\circ\text{C}$

\*\*  $I_{CC}$  is measured with all outputs open and all inputs at 4.5V.

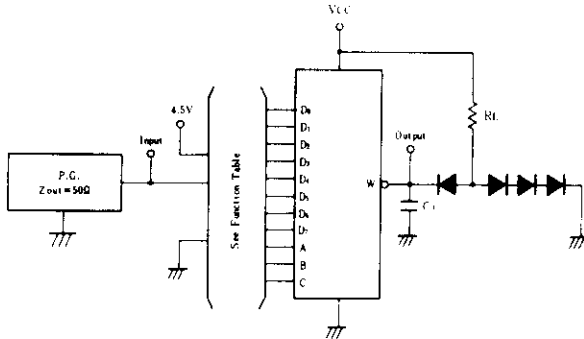
# HD74LS152

## SWITCHING CHARACTERISTICS ( $V_{CC}=5V$ , $T_a=25^{\circ}C$ )

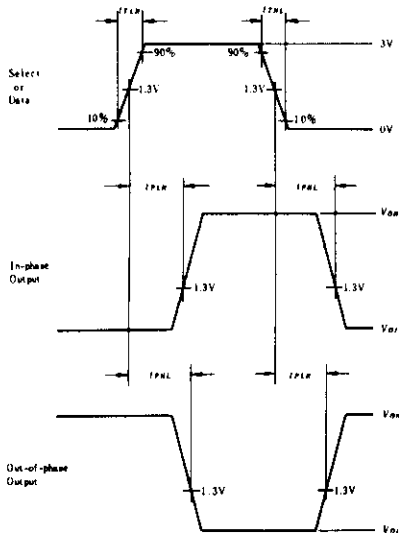
Item	Symbol	Inputs	Outputs	Test Conditions	min	typ	max	Unit
Propagation delay time	$t_{PLH}$	A, B, C	W	$C_L = 15pF$ , $R_L = 2k\Omega$	—	14	23	ns
	$t_{PHL}$				—	20	32	
	$t_{PLH}$	Data	W		—	13	21	
	$t_{PHL}$				—	12	20	

## TESTING METHOD

### 1) Test Circuit



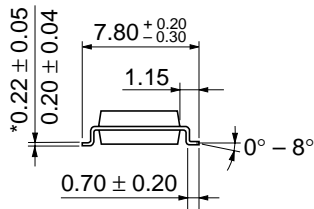
### Waveform



- Notes)
1. Input pulse;  $t_{TLH} \leq 15ns$ ,  $t_{THL} \leq 6ns$ ,  $PRR=1MHz$ , duty cycle=50%
  2.  $C_L$  includes probe and jig capacitance.
  3. All diodes are 1S2074 (H).

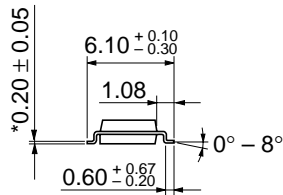
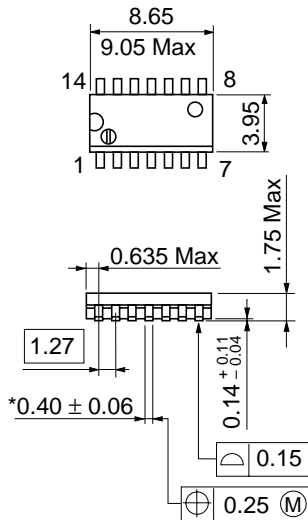


Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g



Hitachi Code	FP-14DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.23 g

\*Dimension including the plating thickness  
Base material dimension



Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

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# HITACHI

## Hitachi, Ltd.

Semiconductor & Integrated Circuits.  
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan  
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL      North America      : <http://semiconductor.hitachi.com/>  
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## For further information write to:

Hitachi Semiconductor  
(America) Inc.  
179 East Tasman Drive,  
San Jose, CA 95134  
Tel: <1> (408) 433-1990  
Fax: <1> (408) 433-0223

Hitachi Europe GmbH  
Electronic components Group  
Dornacher Straße 3  
D-85622 Feldkirchen, Munich  
Germany  
Tel: <49> (89) 9 9180-0  
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.  
Electronic Components Group.  
Whitebrook Park  
Lower Cookham Road  
Maidenhead  
Berkshire SL6 8YA, United Kingdom  
Tel: <44> (1628) 585000  
Fax: <44> (1628) 778322

Hitachi Asia Pte. Ltd.  
16 Collyer Quay #20-00  
Hitachi Tower  
Singapore 049318  
Tel: 535-2100  
Fax: 535-1533

Hitachi Asia Ltd.  
Taipei Branch Office  
3F, Hung Kuo Building, No.167,  
Tun-Hwa North Road, Taipei (105)  
Tel: <886> (2) 2718-3666  
Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd.  
Group III (Electronic Components)  
7/F., North Tower, World Finance Centre,  
Harbour City, Canton Road, Tsim Sha Tsui,  
Kowloon, Hong Kong  
Tel: <852> (2) 735 9218  
Fax: <852> (2) 730 0281  
Telex: 40815 HITEC HX

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