

isc N-Channel MOSFET Transistor

IRFP4668, IIRFP4668

FEATURES

- Static drain-source on-resistance:
 $R_{DS(on)} \leq 9.7m\Omega$
- Enhancement mode:
 $V_{th} = 3.0 \text{ to } 5.0 \text{ V}$ ($V_{DS}=V_{GS}$, $I_D=250 \mu A$)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

DESCRIPTION

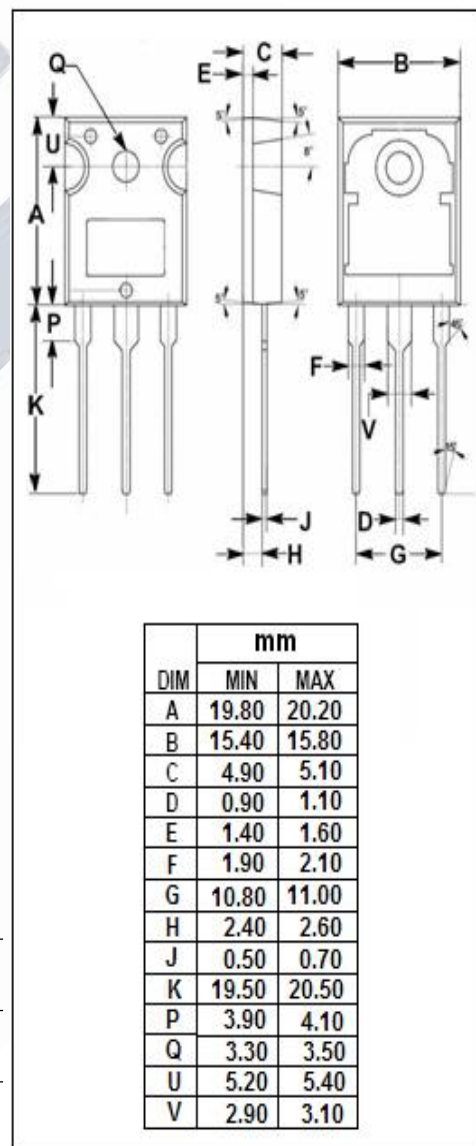
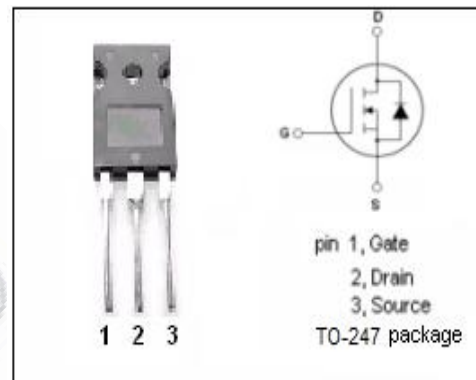
- High Efficiency Synchronous Rectification in SMPS
- Uninterruptible Power Supply
- High Speed Power Switching
- Hard Switched And High Frequency Circuits

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	200	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous	130	A
I_{DM}	Drain Current-Single Pulsed	520	A
P_D	Total Dissipation @ $T_c=25^\circ C$	520	W
T_j	Max. Operating Junction Temperature	175	$^\circ C$
T_{stg}	Storage Temperature	-55~175	$^\circ C$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	0.29	$^\circ C/W$
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	40	$^\circ C/W$



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ELECTRICAL CHARACTERISTICS

 $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DS}	Drain-Source Breakdown Voltage	$V_{GS}=0V; I_D=250\mu A$	200			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}; I_D=250\mu A$	3.0		5.0	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V; I_D=81A$			9.7	$m\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{GS}= \pm 20V$			± 0.1	μA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V; V_{GS}= 0V$			20	μA
V_{SD}	Diode forward voltage	$I_S=81A, V_{GS}= 0V$			1.3	V